



Oasis Technologies Pvt. Ltd.

ARM Video Details

Sr. No.	Video Name	File Size	Duration	
		MB	Hr	Min
1	Introduction, Architecture Block Diagram	63	0	11 Mins
2	Flag Register	90	0	16 Mins
3	Memory Structure	105	0	18 Mins
4	Instruction Set	107	0	19 Mins
5	Logical Instruction	120	0	21 Mins
6	Interrupt block and Instruction Set	106	0	19 Mins
7	Boolean Instruction	77	0	14 Mins
8	Table Read, Unconditional, Branch Instruction	112	0	20 Mins
9	Prog in C, Interrup Structure and Priority Register	156	0	28 Mins
10	Timer Programming	75	0	13 Mins
11	Serial Communication	129	0	23 Mins
12	Delay Count, Device and LCD prog and stepper motor	248	0	44 Mins
13	Keyboard Interfacing, ext memory prog , seven segment	64	0	11 Mins

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Mr. Narhar Dole has rich experience of 35 yrs under Central Government DRDO. He retired as Scientist 'D' from Defence Institute Of Advance Technology Formerly known as Institute for Armament Technology His experience lies in teaching post graduate courses of Microproces Microcontroller and digital system design.